AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1-35. (Canceled)

36. (Original) An electronic equipment comprising:

a processor incorporating a delay circuit element whose delay time changes depending on a temperature;

a detection circuit, connected to said delay circuit element, for detecting an internal temperature of said processor from a change in response delay of said delay circuit element; and

clock control means for controlling a clock signal supplied to said processor such that an operating speed of said processor is decreased when the internal temperature detected by said detection circuit exceeds a first temperature.

37-50. (Canceled)

51. (Currently Amended) An electronic apparatus comprising:

a semiconductor circuit including an element having a characteristic that varies with temperature and outputting a signal corresponding to the characteristic of the element, the semiconductor circuit operating according to a clock frequency;

~~a temperature detecting element configured to detect a temperature of the semiconductor circuit based on the signal of the semiconductor circuit~~ a device which is provided outside of the semiconductor circuit, the device configured to compare a temperature indicated by the signal with predetermined values and issue an event according to a comparison result; and

a system power controller which is provided outside of the semiconductor circuit, the system power controller configured to control the clock frequency of the semiconductor circuit to be decreased when the ~~temperature detected by the temperature detecting element exceeds a first threshold~~ event indicates a first temperature status, and to control the electronic apparatus to be powered off when the ~~temperature detected by the temperature detecting element exceeds a second threshold higher than the first threshold~~ event indicates a second temperature status.

52. (Previously Presented) The electronic apparatus according to claim 51, wherein the temperature detecting element includes a p-n junction circuit element.

53. (Previously Presented) The electronic apparatus according to claim 51, wherein the semiconductor circuit includes a CPU.

54. (Currently Amended) A control method applied to an electronic apparatus having a semiconductor circuit including an element having a characteristic that varies with temperature and outputting a signal corresponding to the characteristic of the element, the semiconductor circuit operating according to a clock frequency, the method comprising:

~~detecting a temperature of the semiconductor circuit based on the signal of the semiconductor circuit~~ comparing a temperature indicated by the signal with predetermined values and issuing an event according to a comparison result by a device provided outside of the semiconductor circuit; and

controlling the clock frequency of the semiconductor circuit to be decreased by a system power controller provided outside of the semiconductor circuit when the ~~temperature detected in the detection exceeds a first threshold~~ event indicates a first temperature status, and controlling the electronic apparatus to be powered off by the system power controller when the ~~temperature detected in the detection exceeds a second threshold higher than the first threshold~~ event indicates a second temperature status.

55. (Currently Amended) An electronic apparatus comprising:

a circuit including an element having a characteristic that varies with temperature and configured to output a signal corresponding to the characteristic of the element;

~~a temperature detection unit configured to detect a temperature of the circuit to output a logical signal outside of the temperature detection unit when the detected temperature exceeds a threshold; and~~

a device which is provided outside of the circuit and configured to compare a temperature indicated by the signal with predetermined values and issue a logical signal according to a comparison result; and

a system power controller which is provided outside the circuit and configured to power off the electronic apparatus ~~based on the logical signal output from the temperature detection unit~~ when the logical signal indicates a certain temperature status.

56. (Previously Presented) The electronic apparatus according to claim 55, wherein the circuit includes a CPU.

57. (Currently Amended) A computer comprising:

a circuit including an element having a characteristic that varies with temperature and configured to output an analog signal corresponding to the characteristic of the element;

~~a temperature detection portion configured to detect a temperature of the circuit and to output an analog signal based on the temperature to outside of the temperature detection portion;~~

~~an analog-digital converter configured to convert the analog signal from the temperature detection portion to a digital signal; and~~

a devicewhich is provided outside of the circuit and configured to compare a temperature indicated by the analog signal with predetermined values and issue a digital signal according to a comparison result; and

a system power controller which is provided outside of the semiconductor circuit and configured to control the computer to be powered off when ~~a value of the digital signal exceeds a threshold value~~ the digital signal indicates a certain temperature status.

58. (Currently Amended) The computer according to claim 57, wherein the temperature detection ~~portion~~ element includes a p-n junction circuit element.

59. (Previously Presented) The computer according to claim 57, wherein the circuit includes a CPU.